

INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Not for submission under 37 CFR 1.99)

Application Number	10675165
Filing Date	2003-09-30
First Named Inventor	Ernest Brickell
Art Unit	2135
Examiner Name	Thanhnga B. Truong
Attorney Docket Number	42P16807

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	2	4974159		1990-11-27	Hargrove, et al.	
	3	6496847		2002-12-17	Bugnion, et al.	
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	2	ROSENBERG, JONATHAN B., "How Debuggers Work Algorithms, Data Structures, and Architecture", Chapters 3 and 5 Hardware Debugger Facilities, Wiley Computer Publishing, United States, 1996, pp. 42-43, 95, 96 and 99.	<input type="checkbox"/>
	3	WENISCH, et al., "Store-Ordered Streaming of Shared Memory", Proceedings of the 14th Int'l Conference on Parallel Architectures and Compilation Techniques (PACT'05) September 2005, IEEE, pages 75-84.	<input type="checkbox"/>

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☐ Fee set forth in 37 CFR 1.17 (p) has been submitted herewith.

☐ None

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A signature of the applicant or representative is required in accordance with CFR 1.33, 10.18. Please see CFR 1.4(d) for the form of the signature.

Signature	/William W. Schaal/	Date (YYYY-MM-DD)	2007-08-07
Name/Print	William W. Schaal	Registration Number	39018

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